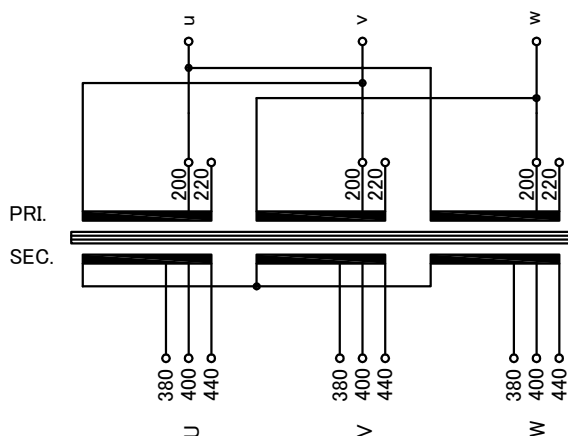


Circuit Diagram



容量 Capacity	40kVA
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Phase	Frequency
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Primary Volt. 1200/120V

Secondary Volt. 000, 1100, 1110V

Secondary Curr.	97.77%
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Class	Mass	200kg
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Insulation Resistance

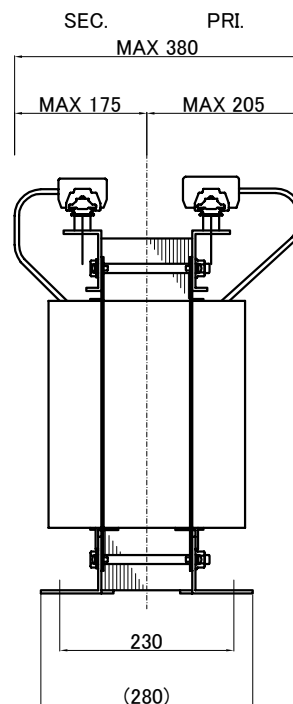
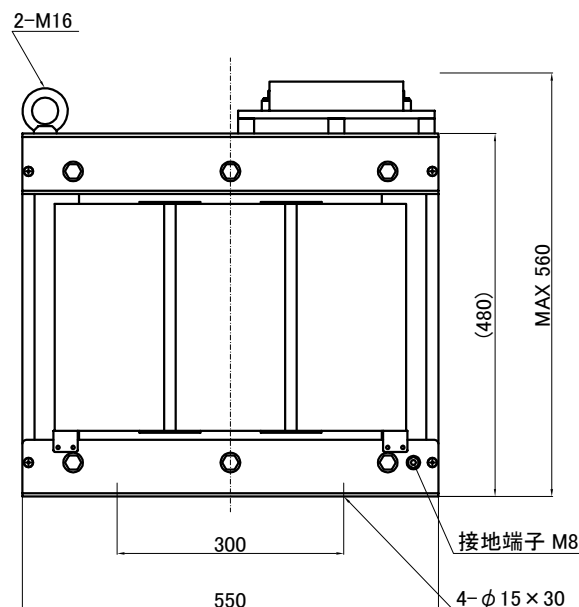
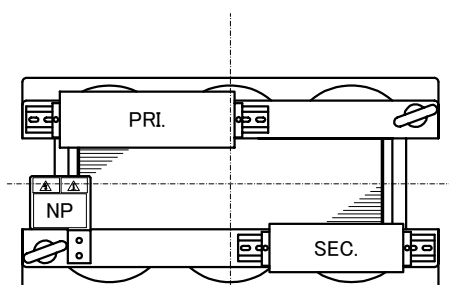
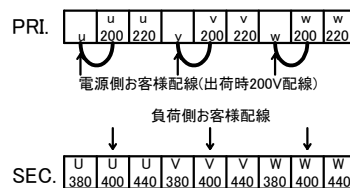
F L F S S L

100M Ω Min. at DC1000V

Dielectric Withstand Volt.

THE UNIVERSITY OF CHICAGO PRESS

Standard 008-2200-2077



今井電機株式会社

DATE 22.11.09

M.Fujitaka

N.Marukai

	mm
東王加理 FINISH	

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SN3D040K2300

改訂
Ver.

[illegible]